



Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5181-97900 APPLICANT: Cavanagh, et al. FILING DATE: November 9, 2001	SERIAL NO. 10/010,572 GROUP: 2123
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U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<i>Appl</i>		5,973,638	10/99	Robbins, et al.	342	172	
<i>Appl</i>		5,466,200	11/95	Ulrich, et al.	482	4	
<i>Appl</i>		5,838,948	11/98	Bunza, Geoffrey J.	703	27	
<i>Appl</i>		6,289,398	9/01	Stallmo, et al.	710	5	
<i>Appl</i>		5,889,954	3/99	Gessel, et al.	709	223	
<i>Appl</i>		4,590,581	5/86	Widdoes, Jr., L. Curtis	703	2	
<i>Appl</i>		5,247,650	9/93	Judd, et al.	703	20	
<i>Appl</i>		5,893,155	4/99	Cheriton, David R.	711	144	
<i>Appl</i>		6,031,907	2/00	Damani, et al.	379	413.04	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>6/8/06</i> <i>Appl</i>	D1	Damani, Om, et al., "Fault-Tolerant Distributed Simulation," May 1998, Univ. Texas, 9 pages.
<i>6/8/06</i> <i>Appl</i>	D2	Tay, Seng Chuan, et al., "Speculative Parallel Simulation with an Adaptive Throttle Scheme, 1997, IEEE, pages 116-123.
<i>6/8/06</i> <i>Appl</i>	D3	Lee, K.C., "A Virtual Bus Architecture for Dynamic Parallel Processing Parallel and Distributed Systems," IEEE Transactions, Vol. 4, Issue 2, February 1993, pages 121-130.
<i>4/8/06</i> <i>Appl</i>	D4	Lee, K.C., "A Virtual Bus for Dynamic Parallel Processing Parallel and Distributed Processing, 1990, Proceedings of the Second IEEE Symposium on December 9-13, 1990, pages 736-743.

EXAMINER:

Appl Sharon

DATE CONSIDERED:

12/19/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

